

# ANALYSIS OF A DOUBLE-WAVE ACCURACY RECTIFIER'S OPERATION WITH OPERATIONAL AMPLIFIERS

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## **Abstract**:

In this work is studied the operation of a double-wave accuracy rectifier achieved with operational amplifiers LM741 at frequencies that exceed their cut-off frequency, using the Multisim simulation program. Is aimed the quality and accuracy of the obtained signal, having in view the damping introduced by the operational amplifiers' operation.

## **Keywords**:

double-wave accuracy rectifier, operational amplifier.

## **1. WORK'S PRESENTATION**

The double-wave rectifier can be obtained by serial connecting of a single-wave rectifier and a summation instrument. Diagram of a single-wave accuracy rectifier which rectifies the positive semiperiods of the input voltage is given in fig. 1. Because it's about small signals, the voltage between the two inputs of the operational amplifier is not neglected anymore, but instead are neglected the OA's



Figure 1. Operation diagram on positive alternance

input currents and the reverse current through the diode is blocking state. Output voltage for the positive semi-period of the input voltage is:

$$u_{e} = -\frac{u_{i} \frac{R_{2}}{R_{1}} + \frac{u_{d1}}{\frac{R_{1}}{R_{1} + R_{2}} A_{u}}}{1 + \frac{1}{\frac{R_{1}}{R_{1} + R_{2}} A_{u}}} \cong -u_{i} \frac{R_{2}}{R_{1}} \qquad (1)$$

At the circuit's output is obtained an identical voltage as time-variation form, and reversed as phase. For the negative semiperiod of the input voltage  $u_i$  diode  $D_1$  is

blocked and diode  $D_2$  conducts all the current coming from the input (fig. 2). In this case, for the output voltage is obtained the relation:



$$u_{e} = -u_{i} \frac{R_{s}}{R_{2} + R_{s}} \cong \frac{u_{d2}}{A_{u}} \cdot \frac{R_{s}}{R_{2} + R_{s}} \cong 0 \quad (2)$$

Is found that, during the negative semiperiod of the input voltage, at output is obtained a voltage almost null.

The diagram of the double-wave rectifier is using such a rectifier, out of which outpt is connected a summation instrument with AO (fig. 3).

Figure2. Opertation diagram on negative alternance







Figure 3. Diagram of the double-wave accuracy rectifier



Figure 4. Electronic diagram of the accuracy rectifier



Figure 5. a) wave forms corresponding to frequencies of 1 kHz; b) parameters corresponding to the wave forms for 1kHz



of 10 kHz; b) parameters corresponding to the wave forms for 10kHz

For the first semi-period (positive) of the input signal, the volatge  $u_{el}$  is negative and, in this case, at the summation device's inputs are brought voltages with equal amplitudes, one positive and one negative, amplified differently. The positive voltage has the amplification 1 and the negative one has the amplification 2, at the summation instrument's output being obtained the positive alternance of the signal  $u_i$ . For the negative alternance of the signal  $u_i$  is obtained  $u_{el} = 0$  and at the summation instrument's input is brought only the negative alternance of the signal  $u_i$ , for  $u_{e2}$  being obtained alternance the reversing and amplification 1.

As result, at output is obtained the input signal's rectification, this without being amplified. If it's desired its amplification, the resistances from the diagram will be modified in the ratio aimed to be obtained.

In order to check the rectifier's operation, it was used a generator of sinusoidal signal with amplitude of 275mV and adjustable frequency (fig. 4). The signal was recorded by means of a digital oscilloscope, which allowed the data acquisition in the memory of a PC system.







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Figure 7. a) wave forms corresponding to frequencies of 10 kHz; b) parameters corresponding to the wave forms for 50kHz

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Are presented three distinct situations, corresponding to all frequencies of 1 KHz (fig. 5.a), 10KHz (fig. 6.a) and respectively 50KHz (fig. 7.a), with the wave forms related to each frequency in part. For each frequency in part are presented the parameters corresponding to the wave forms presented above (fig. 5b, fig. 6b, fig. 7b).

### 2. CONCLUSIONS

Further the experimental verification of the double-wave rectifier, is found a correct operation within a relatively large frequency band, reaching to approximately 50KHz. One can notice that at the end of the negative semi-period is not reaching to zero, but to a value a little superior, which cannot be compensated, because also the other value related to the positive alternance would be reduced identically. Once by increasing the frequency, is found a distortion of the output signal, especially due to the parasite capacities that occur in the circuit, which makes it to integrate the signal brought at input, reaching up to ramp-signal at very high frequencies.

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